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To: Examiner Matthew E. Warren	Total Pages Sent: 4
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Lin, et al.	Docket No.:	TSM03-0670
Serial No:	10/729,092	Art Unit:	2815
Date Filed:	December 5, 2003		
Title:	Structure and Method of Forming Integrated Circuits Utilizing Strained Channel Transistors		

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- Certification of Facsimile Transmission (1 page)
- Information Disclosure Statement (in duplicate = 2 pages)
- IDS Form PTO/SB/08a (1 page) citing nine (9) references

Respectfully submitted,



Natalie Swider
Legal Assistant

Confirmation Respectfully Requested

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Dear Sir:

Applicants wish to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed PTO Form PTO/SB/08a, which may be considered material to the examination of the above-identified application. Copies of the U.S. Patents cited are not being submitted.

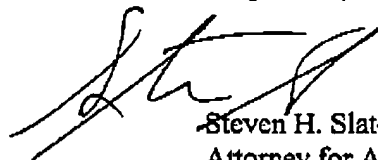
This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first.

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Respectfully submitted,

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